Table 4.6. Opcode times (CPU cycles), for instructions used in `wait()`

Instruction Type	Mnemonic	Clock Cycles
Load/store registers	`LDR, STR`	2
Branch	`BNE, B, BX`	0
Stack	`PUSH, LDMFD`	2
Arithmetic	`ADD, SUB, CMP`	1
Move	`MOV`	1
No operation	`NOP`	1